PERFORMANCE ANALYSIS OF RECURRENT NEURAL NETWORKS (RNN) FOR POWER AMPLIFIER (PA) MODELING

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Duration: 6 months
Degree: Master
Fields: (Computer Science, Electrical Engineering) Machine learning, Optimization, Signal processing, Communication systems
Start date: 2024-04-01

Webpage

You can find all the internship offers at: Job & Internship opportunities at Télécom Paris

The current offer is available at: Performance analysis of recurrent neural networks (RNN) for power amplifier (PA) modeling Please check the webpage for the latest information.

Introduction

This internship project is a continuation of the ICS Mini-Project Teaching the Backpropagation Through Time Algorithm. It is highly recommended to achieve this mini-project to apply for this internship project.

This project is eligible for extension to a PhD thesis.

Project description

PAs are electronics devices that amplify the power of a signal, but they also introduce distortion into the signal. This distortion can be problematic for communication systems, so it is important to have accurate models of PAs in order to design and optimize communication systems.

RNNs are a type of machine learning model that are well-suited for modeling sequential data, such as the time-domain waveforms of PA signals. RNNs have been shown to be effective for modeling the behavior of PAs, but there is still room for improvement.

This project aims to evaluate the performance of different types of RNNs (like GRU or LSTM)
for modeling the behavior of PAs. The training of the RNNs will be done using the BPTT algorithm (hence the requirement to achieve the mini-project).

The project will give students the opportunity to learn about the latest advances in RNN technology and to apply their knowledge to a real-world problem. The project will also give students the opportunity to develop their research and development skills.

In addition, the project is likely to be of interest to industry partners, so students who complete this project may have the opportunity to pursue employment opportunities after their internship.

**Required skills**

This project requires a good knowledge of machine learning concepts, such as neural networks, gradient descent, and backpropagation.

- **Mandatory**
  - Signal processing
  - Matlab programming experience (matrix manipulation, computing, programmatic plotting)
  - practical elements of Latex (writing equations)
  - practical elements of git
  - Linux OS basics (usage of terminal command lines, ssh, make,...)
  - BPTT algorithm (see the mini-project)

**Workplan (6 months)**
Application

Please send your application to the internship supervisor (please see headings). Your application should include:

- a CV,
- a cover letter,
- your academic records,
- a recommendation letter from a professor or a previous internship supervisor.

**Deadline for application: 1st March 2024.**

Upon reception of your application, we will contact you for an interview. The interview agenda is usually as follows:

<table>
<thead>
<tr>
<th>Duration</th>
<th>Activity</th>
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<tbody>
<tr>
<td>15 min</td>
<td>Presentation of the candidate’s academic (and professional) background to highlight the skills, experiences and any element relevant to the internship</td>
</tr>
<tr>
<td>15 min</td>
<td>Presentation of the internship project and the host team by the supervisor</td>
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<tr>
<td>15 min</td>
<td>Open discussion</td>
</tr>
<tr>
<td>10 min</td>
<td>Short test on either signal processing or Matlab programming</td>
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<tr>
<td>10 min</td>
<td>Discussion on the short test</td>
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</tbody>
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School

Télécom Paris trains its students to innovate in today’s digital world. Its training and research cover all fields of information and communication sciences and technologies with a strong societal foundation in order to address the major challenges of the 21st century. Its offers engineering, PhD and professional degree programs, with international students accounting for 55% of its student body. Its research offers original, multidisciplinary world-class expertise in nine strategic areas: Data Science and Artificial Intelligence — Visual and Audio Computing, Interaction — Digital Trust — Innovation Regulations — Transformation of Innovative Firms — Cyber-Physical Systems — Communication Systems and Networks — Mathematics and Applications — Uses, Participation, Democratization of Innovation.

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Address: 19 place Marguerite Perey, 91120 Palaiseau, France

Hosting laboratory

Laboratoire Traitement et Communication de l’Information (LTCI)

Research team

The Circuits et Systèmes de Communication (C2S) team is internationally recognized for its ability to integrate digital intelligence into AMS and RF SoCs such as analog-to-digital converters (ADCs) or RF receivers for cognitive radio. By combining its expertise in the physical realization of the CMOS chip with its experience in signal processing and its knowledge of the other network layers for which LTCI’s skills are recognized, the group designs high-performance AMS and RF SoCs. The aim is to develop elements or “building blocks”, enabling the system of connected objects to be interfaced on one side with the physical world via sensors, and on the other side with the system core via communications, in particular RF.

References

In addition to the references of the related mini-project, literature review will focus on the following papers:

https://doi.org/10.48550/arXiv.1801.01078

FAQ

Will I be paid?
You will receive a stipend, the amount is approximately 350€/month.

How to accommodate my stay in France?
There are several student residences in the vicinity of the campus. Further information will be provided upon demand.